

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: KAJIHARA et al.

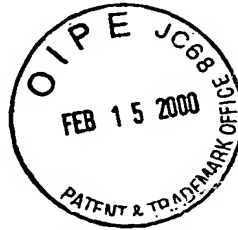
Serial No.: 09/328,910

Filed: June 9, 1999

For: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
USING THE SAME, AND METHOD OF AND PROCESS FOR
FABRICATING THE TWO

Art Unit: 2811

Examiner: Unassigned



PETITION FOR EXTENSION OF TIME

Assistant Commissioner
for Patents
Washington, D.C. 20231

February 15, 2000

Sir:

In the matter of the above-identified application, Applicant hereby respectfully petitions for an extension of time to permit filing a response within the fifth month subsequent to expiration of the shortened statutory period set in the outstanding Notice to File Missing Parts of Application mailed July 15, 1999. A check including the amount of \$1,850.00 to cover the required fee for the requested extension of time is attached hereto.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (referencing Case No. 501.32049R00).

02/17/2000 YGIZAW 00000022 09328910

01 FC:128

1850.00 OP

Respectfully submitted,

Paul J. Skwierawski

Registration No. 32,173

ANTONELLI, TERRY, STOUT & KRAUS, LLP

PJS/pay
(703) 312-6600